

In the Claims

Claim 17 (Currently Amended): An image detection processor comprising:

an image detection processing element array which includes a plurality of image detection processing elements, each of the image detection processing elements including a photodetector, a converter for converting signals from said photodetector into digital signals, and an adder which receives said digital signal and output of a precedent image detection processing element, and outputs an added signal to a next image detection processing element;

a plurality of control lines which are connected to the image detection processing elements;

a control circuit which selectively inputs digital signals of the image detection processing elements to a corresponding adder by controlling the ~~row and column lines~~ plurality of control lines; and

an output port to which processed data outputted from an arbitrary adder is supplied in response to image data detected by said photodetector.